



JPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/755,734
Filed: January 12, 2004
Inventor(s):
Isaac et al.

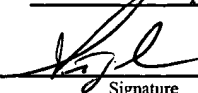
Examiner: Daniel Y. Kim
Group/Art Unit: 2185
Atty. Dkt. No: 5500-91000

Title: CACHE MEMORY
SUBSYSTEM
INCLUDING A FIXED
LATENCY R/W PIPELINE

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Stephen J. Curran

Printed Name



Signature

6/12/06

Date

RESPONSE TO OFFICE ACTION OF
March 16, 2006

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of March 16, 2006, to further highlight why the application is in condition for allowance.